

CALIBRATION AND TESTING ARCHITECTURE FOR RECEIVERS

Abstract

A method and apparatus are provided to generate calibration signals to
5 multiple stages in a receiver channel. The multiple stages are calibrated using
multiple calibration circuits, where a controller controls each calibration circuit.
The controller is coupled to the output of the final stage in the receiver channel
through a single comparison unit. The output from the single comparison unit is
used by the controller to calibrate each of the multiple stages.

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"Express Mail" mailing label number: EV415949217US

Date of Deposit: March 30, 2004

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